

PN

COPY OF PAPERS
ORIGINALLY FILED

#303CO



U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

**TRANSMITTAL OF FORMAL
DRAWINGS**

Docket Number:
2207/12116

Application Number
10/016,793

Filing Date
October 26, 2001

Examiner
Not yet assigned

Art Unit
2838

Invention Title
**SILICON INTERPOSER-BASED HYBRID
VOLTAGE REGULATOR SYSTEM FOR VLSI
DEVICES**

Inventor(s)
NAIR, et al.

Address to:

Commissioner for Patents
Washington D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Box Missing Parts, Washington, D.C. 20231 on

Date: 6/27/02

Signature: Howard Grossman

We are enclosing herewith a complete set of formal drawings for the above-identified application.

Dated: 6/27/02

Howard Grossman
Howard I. Grossman, Reg. No. 48,673



26646

PATENT TRADEMARK OFFICE

KENYON & KENYON
One Broadway
New York, N.Y. 10004
(212) 425-7200 (telephone)
(212) 425-5288 (facsimile)

© Kenyon & Kenyon 2001

2001/06/27 10:00